

Electronic Acknowledgement Receipt

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| EFS ID: | 2477457 |
| Application Number: | 10083872 |
| International Application Number: | |
| Confirmation Number: | 4721 |
| Title of Invention: | Multi-cluster processor for processing instructions of one or more instruction threads |
| First Named Inventor/Applicant Name: | Eric DeLano |
| Correspondence Address: | HEWLETT-PACKARD COMPANY Intellectual Property Administration P.O. Box 272400 - Fort Collins CO 80527-2400 US 9708983886 - |
| Filer: | Kyle J. Way/Jamie Cameron |
| Filer Authorized By: | Kyle J. Way |
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| Application Type: | Utility under 35 USC 111(a) |

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| File Listing: | |

| Document Number | Document Description | File Name | File Size(Bytes) /Message Digest | Multi Part /.zip | Pages (if appl.) |
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| 1 | | ResponsetoFinalOA.pdf | 85339 <small>d7b28ea026a3d89c284ee6db3d5c0a77a477e60</small> | yes | 9 |
| | Multipart Description/PDF files in .zip description | | | | |
| | Document Description | | Start | | End |
| | Fee Worksheet (PTO-06) | | 1 | | 1 |
| | Amendment After Final | | 2 | | 9 |
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